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WIRED FIXTURE DATA REQUIREMENTS

In order to insure both “on schedule” and “error free” test fixtures, the following “customer supplied” information is required at time of order. Incomplete data packages will cause delay in processing the order and may result in an order that cannot be completed. Orders will not be processed without a complete manufacturing package. **INCOMPLETE PACKAGES MAY INCUR ADDITIONAL COSTS.**

THE FOLLOWING DATA IS REQUIRED FOR ALL FIXTURE TYPES:

LOADED BOARD _____

- a) The board revision must be the same as that which will be used on the test fixture.
- b) The board must be assembled in the same configuration as that which will be used on the test fixture.

BARE BOARD _____

- a) The board revision must be the same as that which will be used on the test fixture.

GERBER FILES WITH APERTURE LIST _____

- a) The Gerber files must be the same revision as the UUT. RS-274-X is the preferred format.
- b) Gerber files are required for silk-screen information, probe placement and UUT support placement.
- c) Gerber data is required even in the event that probe placement coordinates are supplied by the customer.

CAD ASCII DATABASE FILE _____

- a) The CAD ASCII database file is used to determine the “pin outs” of all devices. The file is required for all fixtures that require UNI-FIX to determine probe placement. **IPC-D-356-A is the preferred format.**
- b) Additional costs may apply if this file is not supplied.

PCB FABRICATION DRAWING _____

- a) The board FAB drawing is required to determine all tolerances in the UUT. The FAB drawing must include board and tooling hole sizes and tolerances. **DXF is the preferred format.**

PCBA ASSEMBLY DRAWING _____

- a) The assembly drawing is required to assist UNI-FIX in probe placement, as well as placement of additional devices such as Opens X-Press, TestJet, VTEP, FrameScan, Wavescan, etc. **PDF or DXF are the preferred formats.**

SIGNAL WIRE LIST _____

- a) A signal wire list is required for all fixture types. **ASCII text is the preferred format.**
- b) Signal wire list format is system dependent. Please see below for specific system file requirements.

POWER WIRE LIST _____

- a) A power wire list is required for all fixture types. **ASCII text is the preferred format.**
- b) Power wire list format is system dependent. Please see below for specific system file requirements.

OPENS X-PRESS, TESTJET, VTEP, FRAMESCAN or WAVESCAN FILE _____

- a) Wiring information is required for all multi-scan devices. This information must include the devices to be scanned and the interface wiring of these devices. **ASCII text is the preferred format.**

SCHEMATICS FOR ADDITIONAL DEVICE WIRING _____

- a) If additional devices are required in the test fixture, a parts list and wiring schematic are required. **PDF is the preferred format.**

STATEMENT OF WORK _____

- a) A “Statement of Work” is required for all items that are specified by the customer. This includes probe style, probe manufacturer, wire gauge, ESD requirements, silk-screen requirements, etc. If the customer supplies no “Statement of Work”, it is understood that UNI-FIX standards shall apply. **Microsoft WORD Document is the preferred format.**

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SYSTEM SPECIFIC DATA REQUIREMENTS:

Teradyne (GenRad) 227x/228x

Wire List _____

- a) The GenRad "NAR" file is required. This file is required even in the event that the customer uses CBTest, Alchemist or D2B and generates fixture load/wire files.

Power List _____

- a) The GenRad "FWI" file is required for wiring of power supplies. If this file is not supplied, the customer must furnish UNI-FIX with a list that specifies which power supplies will be used, the value of the power supply to be used and reference either the "nail number" or "net name" from the "NAR" file that it will connect to.

Opens X-Press List _____

- a) The GenRad "DPR" file is required for all Opens X-Press wiring.

Agilent 3070

Wire List/Drilling Information _____

- a) The minimum compliment of files for the 3070 must include the following from the "FIXTURE" directory: BOARD, DETAILS, DRILL, DRILLTOP (for dual-side access fixtures), FIXTURE.O, INSERTS, SUMMARY, TRACE and WIRES

TestJet or VTEP List _____

- a) The file "TESTJET" or "VTEP" must be included for all TestJet or VTEP wiring.

Teradyne 18xx

Wire List _____

- a) A wire list indicating the Teradyne nail numbers along with a "complete" list of the nets (components/pins) they attach to is required. A partial list or single pin list for each net is not acceptable.

Power List _____

- a) A list of all power supplies to be used and the corresponding net from the fixture wire list is required.

WaveScan, DeltaScan, FrameScan, CapScan, MultiScan List _____

- a) A list of all "scan" devices and the wiring of these devices is required.

Teradyne L2xx/L3xx

Wire List _____

- a) The Teradyne "FXW" file is required. In addition, a complete net list must be supplied.

Power List _____

- a) The "PWR" file is required for all power wiring.

Teradyne Spectrum

Wire List _____

- a) A wire list indicating the Teradyne nail numbers along with a "complete" list of the nets (components/pins) they attach to is required. A partial list or single pin list for each net is not acceptable.

Power List _____

- a) A list of all power supplies to be used and the corresponding net from the fixture wire list is required.

WaveScan, DeltaScan, FrameScan, CapScan, MultiScan List _____

- a) A list of all "scan" devices and the wiring of these devices is required.

TURN-KEY TEST PROGRAM AND FIXTURE

The following information is required to develop "turn-key" test program and fixture orders.

BILL-OF-MATERIALS (BOM) _____

- a) An electronic format Bill-of-Materials list is required. The format of preference for this file is Microsoft Excel. BOM must be for the current revision of the PCBA to be tested.

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ASSEMBLY SCHEMATICS _____

- a) A complete set of schematics must be supplied, including all “stuffing” variations of the board to be tested. An electronic format, Adobe Acrobat “.PDF” file is preferred. Schematics must be for the current revision of the PCBA to be tested.

COMPLETE CAD DATA _____

- a) A complete, current revision, CAD database file is required.

NET LIST _____

- a) An electronic format net list is required. The net list must be for the current revision of the PCBA to be tested. The net list must include ALL components on the PCBA.

ADDITIONAL FILES _____

- a) JEDEC files for all PALs and data files for all FPGAs are required. Additional data may be required, depending on modeling requirements for devices that are not in the target tester’s library.

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- c)

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All information may be supplied on CD, DVD or may be forwarded in compressed format to engineering@uni-fix.com